

19

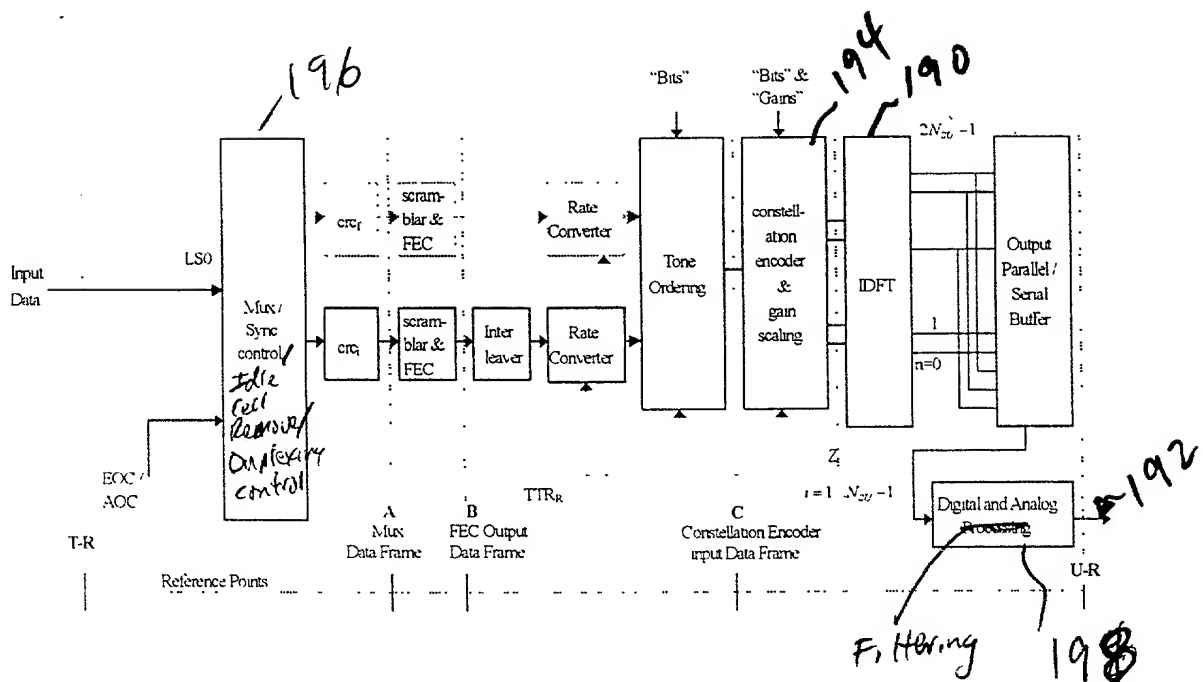


FIGURE 1 ATU-R TRANSMITTER MODEL

10

15

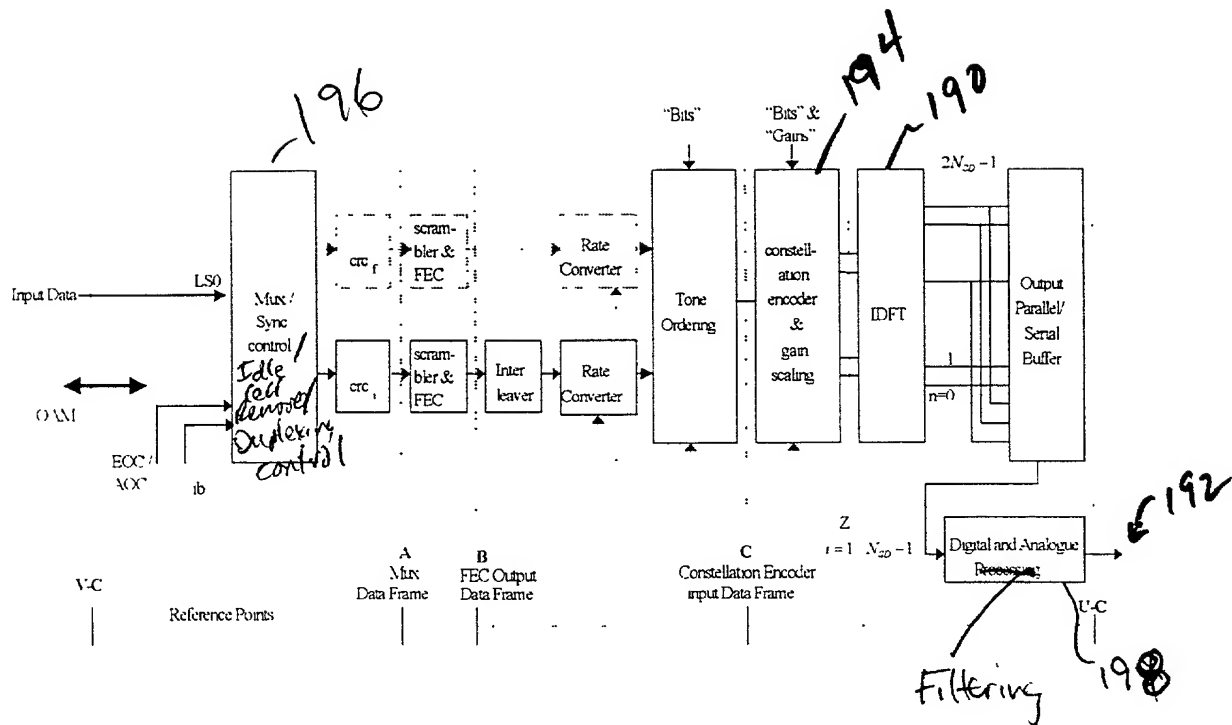


FIGURE 2 ATU-C TRANSMITTER MODEL

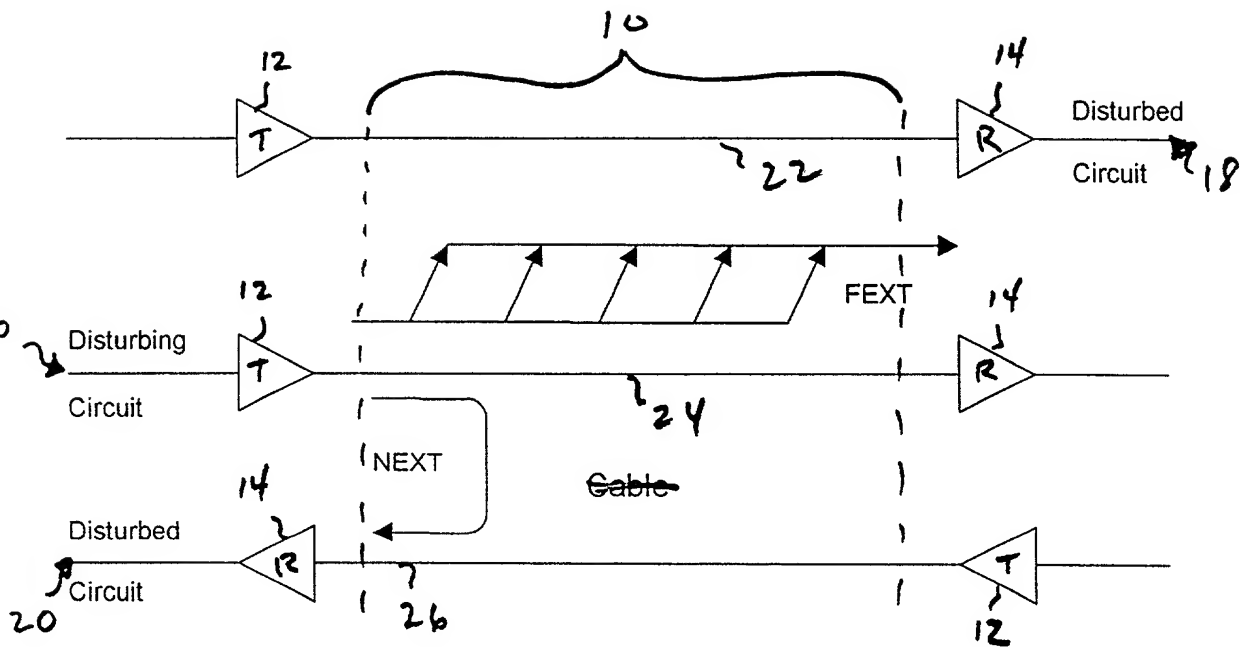


FIGURE 3. FAR-END CROSSTALK AND NEAR-END CROSSTALK COUPLING IN TYPICAL SUBSCRIBER LOOPS

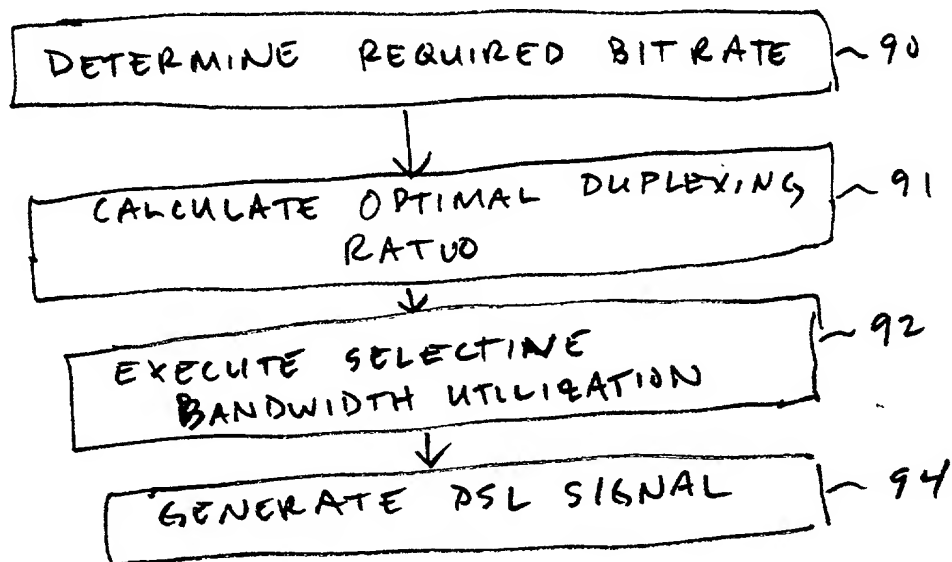
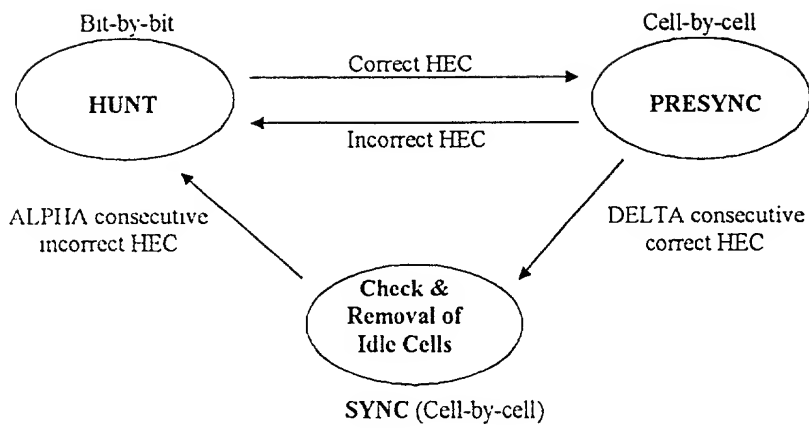


FIG. 4



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FIGURE 40 ATM IDLE CELLS REMOVAL STATE MACHINE

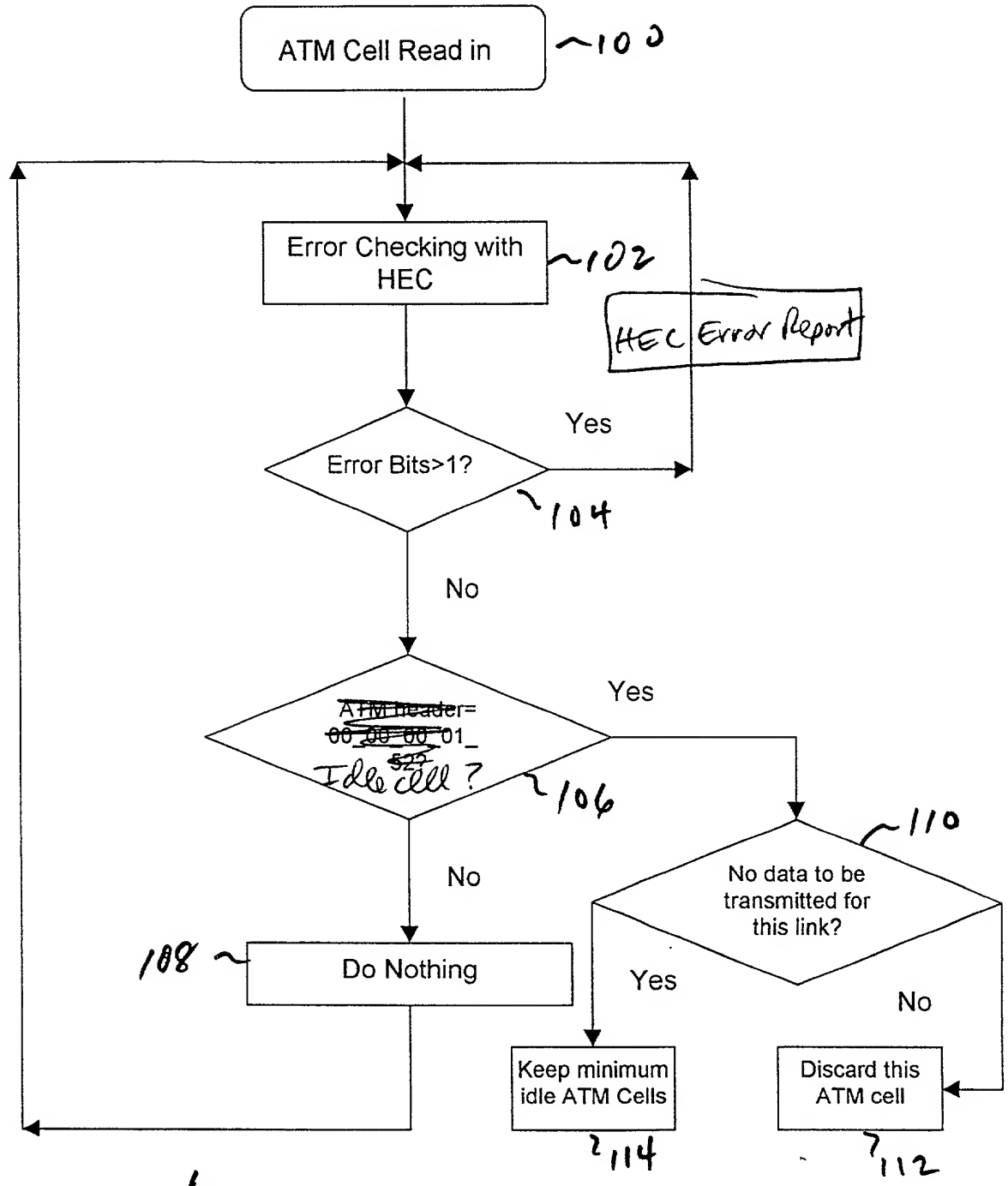
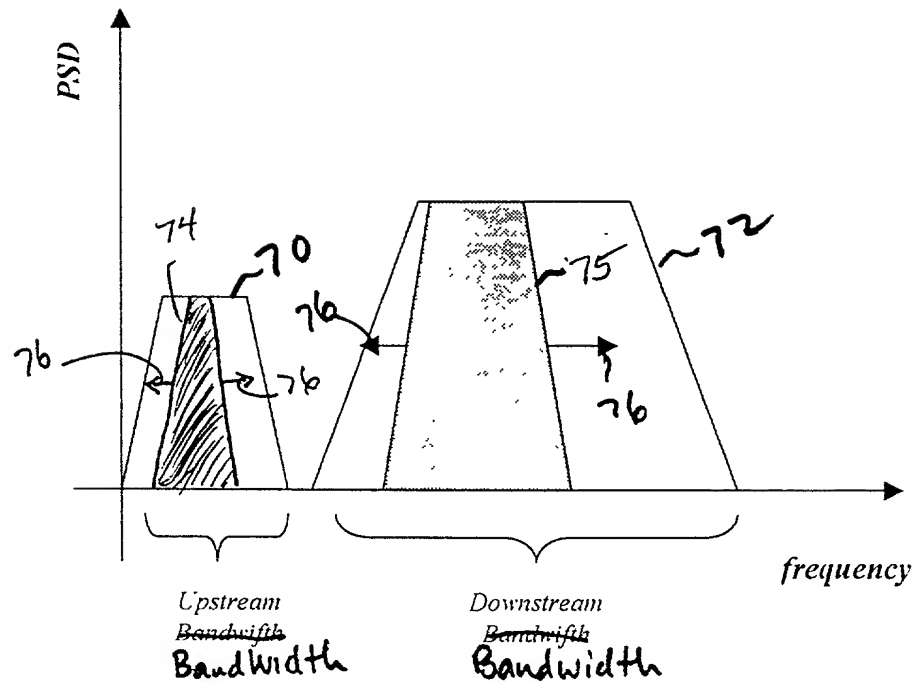


FIGURE 5. ATM IDLE CELL REMOVAL PROCEDURE.



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 FIGURE 6. MINIMIZATION OF NEXT NOISE FOR THE CPE RECEIVER
 BY SLIDING WINDOW METHOD. THE SHADED REGION IS THE NEEDED
 BANDWIDTH TO SUPPORT THE DOWNSTREAM BIT RATE.

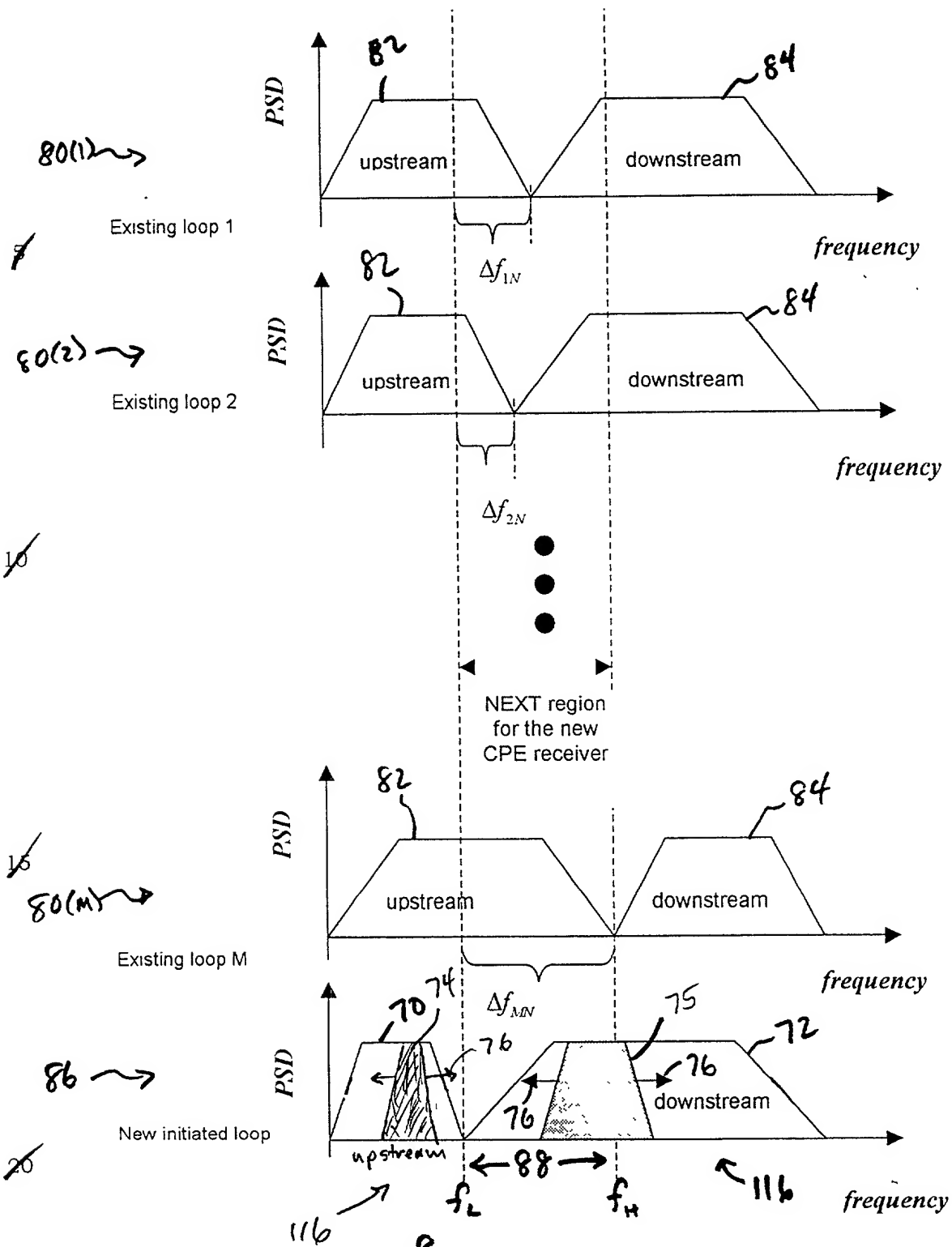


FIGURE 8 NON-OVERLAPPED SYSTEM.

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120(1) ~

Existing loop 1

120(2) ~

Existing loop 2

FOOTNOTES

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120(M) ~

Existing loop M

126 ~

New initiated loop

20

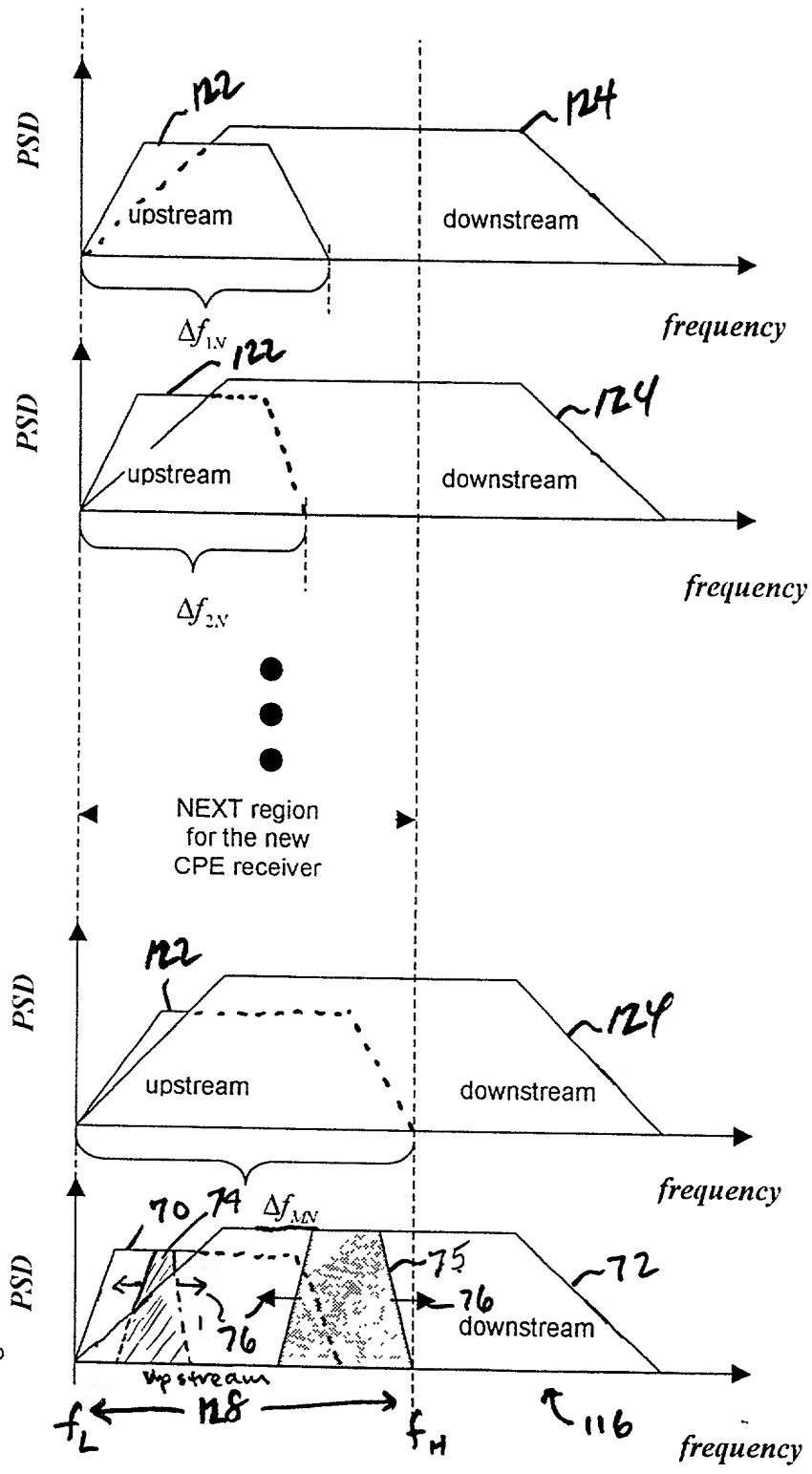


FIGURE 9. OVERLAPPED SYSTEM.

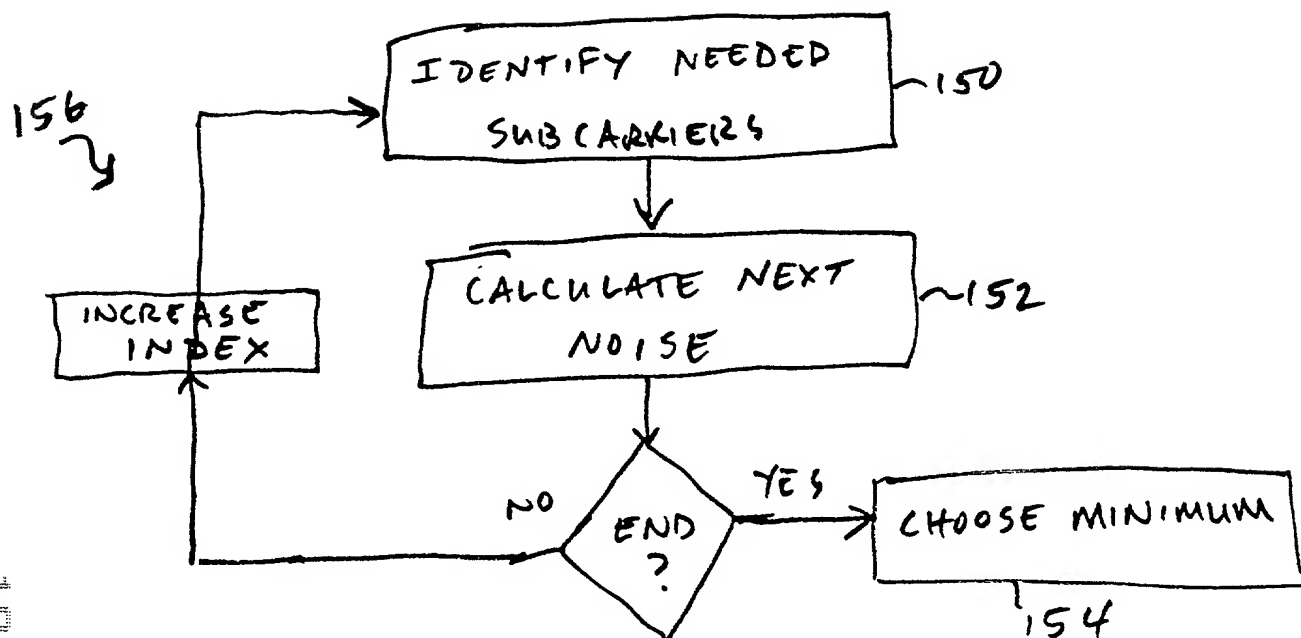


FIG. 10